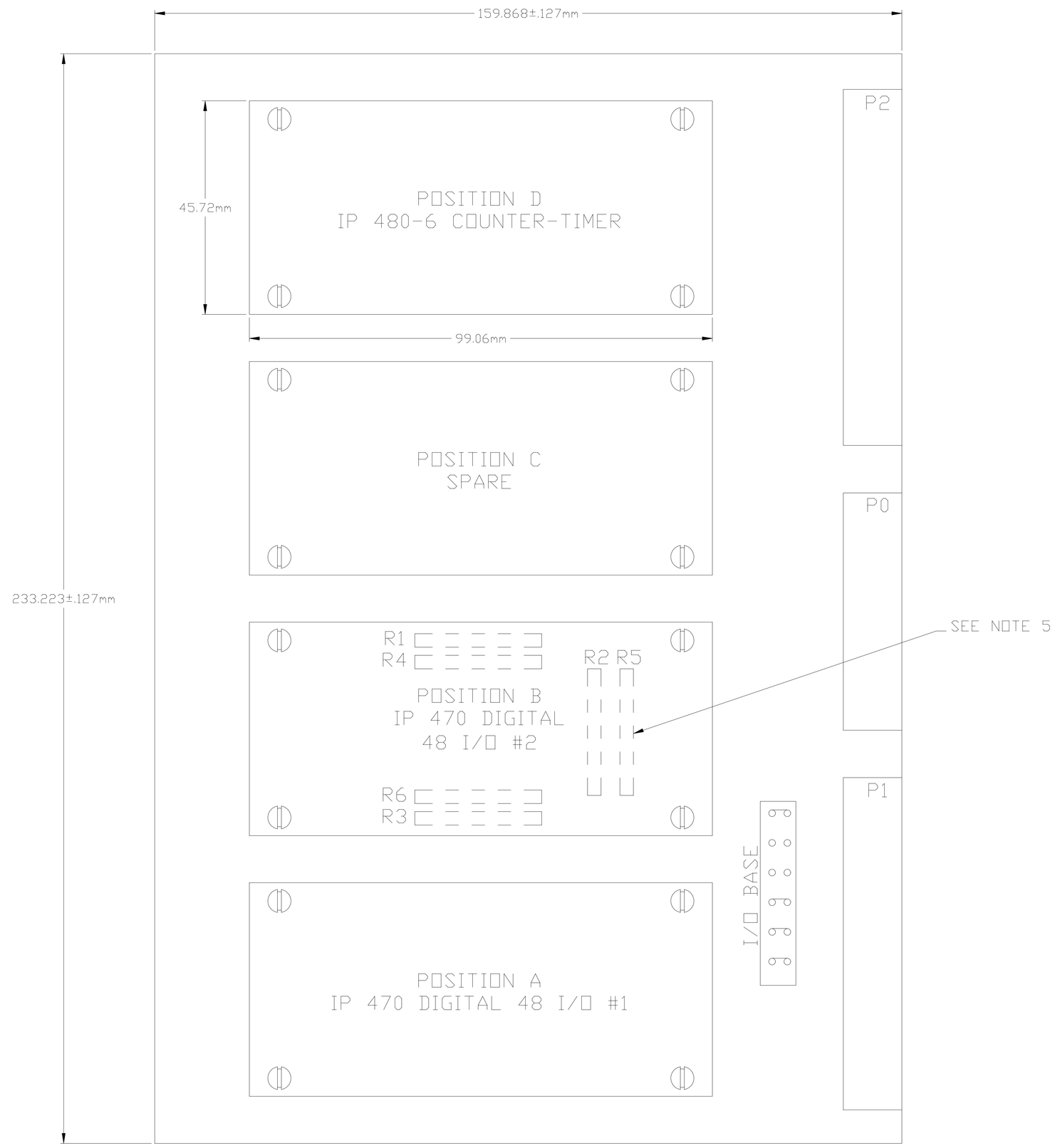


IP CARRIER BOARD VIPC664- SLOT 3

MATERIAL BLOCK		
SYMBOL	MANUFACTURE TYPE	MIL-NO.
CARRIER BOARD	SBS GREENSPRING VIPC664-ET	
IP480-6 COUNTER-TIMER	ACROMAG IP480-6	
IP470 DIGITAL I/O	ACROMAG IP470	

CABLE TIES SHALL BE IN ACCORDANCE WITH NSTS 22648 FOR FLAMMABILITY AND OUTGASSING REQUIREMENTS.

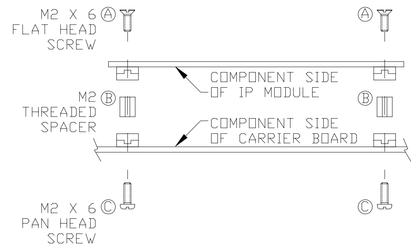
MOUNTING CLAMPS SHALL BE PANDUIT NO.MBMS-S10-C,AL SHEET PER FED SPEC QQ-A-250/2,3003 H0144 OR FABRICATED TO AL SHEET PER FED SPEC QQ-A-250/11,6061 T6.

* COLORS AS REQUIRED.

1. SET THE I/O BASE ADDRESS AS SHOWN IN THE DRAWING.

IP MODULE TO CARRIER BOARD MECHANICAL ASSEMBLY PROCEDURE:

2. INSERT FLAT HEAD SCREWS (ITEM A) THROUGH SOLDER SIDE OF IP MODULE AND INTO HEX SPACERS (ITEM B) AND TIGHTEN (4 PLACES) UNTIL HEX SPACER IS COMPLETELY SEATED.
3. CAREFULLY ALIGN IP MODULE TO CARRIER BOARD AND PRESS TOGETHER UNTIL CONNECTORS AND SPACERS ARE SEATED.
4. INSERT PAN HEAD SCREWS (ITEM C) THROUGH SOLDER SIDE OF CARRIER BOARD AND INTO HEX SPACERS (ITEM B) AND TIGHTEN (4 PLACES).



5. REMOVE R1, R2, R3, R4, R5 AND R6 RESISTOR SIPS.

AUTHORIZED by CM when under FORMAL Configuration Control

CHG	ZONE	DESCRIPTION	APP/DATE

REVISIONS

PROJECT CIR

ANALEX

UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE IN INCHES

National Aeronautics and Space Administration
John H. Glenn Research Center
Lewis Field
Cleveland, Ohio
CONTRACT NUMBER: NAS3-99155

TOLERENCE ON	
.X	+/-
.XX	+/-
.XXX	+/-
FRACTION	+/-
ANGULAR	+/-
SOFTWARE: AUTOCAD 2000	

APPROVALS			
PROJECT LEAD	ANALEX/MD'TOOLE		
ELECTRICAL LEAD	ANALEX/S.LAWYER		
ELECTRICAL ENGINEER	ANALEX/M.BOMO		
CHECKER	X		
DESIGNER	ANALEX/A.JOHNSON		
MAT'L PROC.	X		
CONFIG. MGT	X		

FILE NAME: 67212EELM141.DWG

FDC FEDERAL DATA CORPORATION

DRAWING NAME
FCU IP CARRIER BOARD
ASSEMBLY DRAWING SLOT 3

FLUIDS COMBUSTION FACILITY

SIZE	CAGE CODE	DRAWING NUMBER	REV.
E	X	67212EELM141	1

SCALE: NONE | RELEASE DATE 10/19/00 | SHEET 1 OF 1